6-Pin DIP Schmitt Trigger **Output Optocoupler**

H11L1M, H11L2M, H11L3M

Description

The H11LXM series has a high-speed integrated circuit detector optically coupled to a gallium-arsenide infrared emitting diode. The output incorporates a Schmitt trigger, which provides hysteresis for noise immunity and pulse shaping. The detector circuit is optimized for simplicity of operation and utilizes an open-collector output for maximum application flexibility.

Features

- High Data Rate, 1 MHz Typical (NRZ)
- Free from Latch-up and Oscillation Throughout Voltage and **Temperature Ranges**
- Microprocessor Compatible Drive
- Logic Compatible Output Sinks 16 mA at 0.4 V Maximum
- Guaranteed On/Off Threshold Hysteresis
- Wide Supply Voltage Capability, Compatible with All Popular Logic Systems
- Safety and Regulatory Approvals:
 - ♦ UL1577, 4,170 VAC_{RMS} for 1 Minute
 - DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

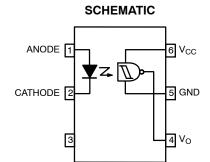
Applications

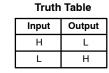
- Logic-to-Logic Isolator
- Programmable Current Level Sensor
- Line Receiver Eliminate Noise and Transient Problems
- AC to TTL Conversion Square Wave Shaping
- Digital Programming of Power Supplies
- Interfaces Computers with Peripherals

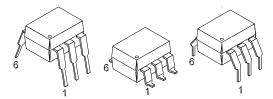


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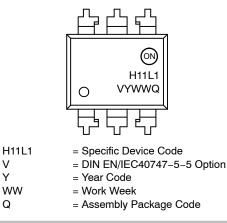
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MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

SAFETY AND INSULATION RATINGS

As per DIN EN/IEC 60747–5–5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter		Characteristics
Installation Classifications per DIN VDE	< 150 V _{RMS}	I–IV
0110/1.89 Table 1, For For Rated Mains Voltage	< 300 V _{RMS}	I–IV
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index	omparative Tracking Index	

Symbol	Parameter	Value	Units
V _{PR}	Input-to-Output Test Voltage, Method A, V _{IORM} × 1.6 = V _{PR} , Type and Sample Test with t_m = 10 s, Partial Discharge < 5 pC	1360	V _{peak}
	Input–to–Output Test Voltage, Method B, V _{IORM} × 1.875 = V _{PR} , 100% Production Test with t_m = 1 s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥0.5	mm
Τ _S	Case Temperature (Note 1)	175	°C
I _{S,INPUT}	Input Current (Note 1)	350	mA
P _{S,OUTPUT}	Output Power (Note 1)	800	mW
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V (Note 1)	>10 ⁹	Ω

1. Safety limit values - maximum values allowed in the event of a failure.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameters	Value	Units
TOTAL DEVIC	E		
T _{STG}	Storage Temperature	-40 to +125	°C
T _{OPR}	Operating Temperature	-40 to +85	°C
ТJ	Junction Temperature	-40 to +125	°C
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C
PD	Total Device Power Dissipation at 25°C	250	mW
	Derate Above 25°C	2.94	mW/°C
MITTER			
١ _F	Continuous Forward Current	30	mA
V _R	Reverse Voltage	6	V
I _F (pk)	Forward Current – Peak (1 µs pulse, 300 pps)	100	mA
PD	LED Power Dissipation	60	mW
DETECTOR			
PD	Detector Power Dissipation	150	mW
Vo	V ₄₅ Allowed Range	0 to 16	V
V _{CC}	V ₆₅ Allowed Range	3 to 16	V
Ι _Ο	I ₄ Output Current	50	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Symbol Parameter Test Conditions Device	Min	Тур	Max	Units]
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INDIVIDUAL COMPONENT CHARACTERISTICS

Emitter

V _F	Input Forward Voltage	l _F = 10 mA	All		1.2	1.5	V
		l _F = 0.3 mA		0.75	1.0		
I _R	Reverse Current	V _R = 3 V	All			10	μΑ
CJ	Capacitance	V = 0, f = 1.0 MHz	All			100	pF

Detector

V _{CC}	Operating Voltage Range		All	3		15	V
I _{CC(off)}	Supply Current	$I_{F} = 0, V_{CC} = 5 V$	All		1.6	5.0	mA
I _{OH}	Output Current, High	$I_F = 0, V_{CC} = V_0 = 15 V$	All			100	μA

TRANSFER CHARACTERISTICS

DC Characteristics

I _{CC(on)}	Supply Current	$I_{F} = 10 \text{ mA}, V_{CC} = 5 \text{ V}$	All		1.6	5.0	mA
V _{OL}	Output Voltage, Low	$\label{eq:RL} \begin{array}{l} R_L = 270 \ \Omega, \ V_{CC} = 5 \ V, \\ I_F = I_{F(on)} \ \text{max}. \end{array}$	All		0.2	0.4	V
I _{F(on)}	Turn-On Threshold Current (Note 2)	R_L = 270 Ω , V_{CC} = 5 V	H11L1M			1.6	mA
			H11L2M			10.0	
			H11L3M			5.0	
I _{F(off)}	Turn-Off Threshold Current	R_L = 270 Ω , V_{CC} = 5 V	All	0.3	1.0		mA
I _{F(off)} /I _{F(on)}	Hysteresis Ratio	$R_L = 270 \ \Omega$, $V_{CC} = 5 \ V$	All	0.50	0.75	0.90	

AC Characteristics, Switching Speed

t _{on}	Turn-On Time	$ \begin{array}{l} R_L = 270 \; \Omega, \; V_{CC} = 5 \; V, \\ I_F = \; I_{F(on)}, \; T_A = 25^\circ C \end{array} $	All	1.0	4.0	μs
t _f	Fall Time	$I_F = I_{F(on)}, I_A = 25^{\circ}C$	All	0.1		
t _{off}	Turn-Off Time		All	1.2	4.0	
tr	Rise Time		All	0.1		
	Data Rate		All	1.0		MHz

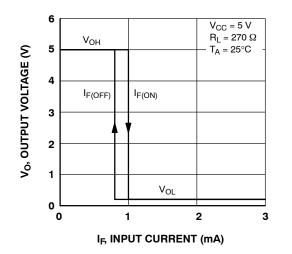
ISOLATION CHARACTERISTICS

V _{ISO}	Input-Output Isolation Voltage	t = 1 Minute	4170			VAC _{RMS}
C _{ISO}	Isolation Capacitance	V _{I-O} = 0 V, f = 1 MHz		0.4	0.6	pF
R _{ISO}	Isolation Resistance	V _{I-O} = ±500 VDC, T _A = 25°C	10 ¹¹			Ω

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product

 Maximum IF(ON) is the maximum current required to trigger the output. For example, a 1.6 mA maximum trigger current would require the LED to be driven at a current greater than 1.6 mA to guarantee the device turns on. A 10% guard band is recommended to account for degradation of the LED over its lifetime. The maximum allowable LED drive current is 30 mA.

TYPICAL PERFORMANCE CURVES





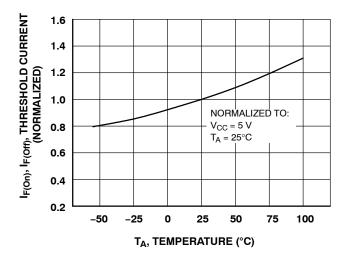


Figure 3. Threshold Current vs. Supply Temperature

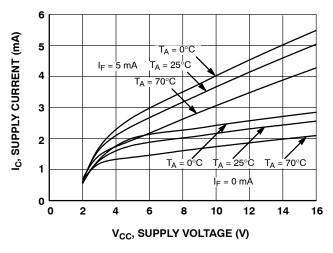
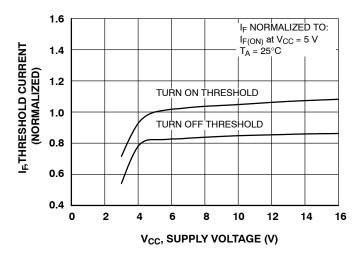
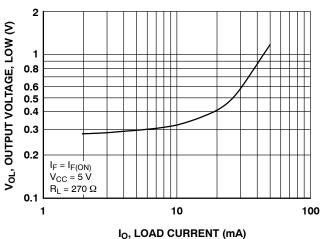


Figure 5. Supply Current vs. Supply Voltage









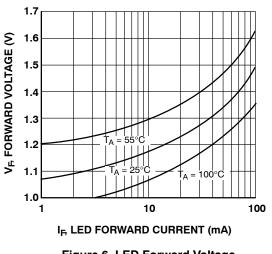
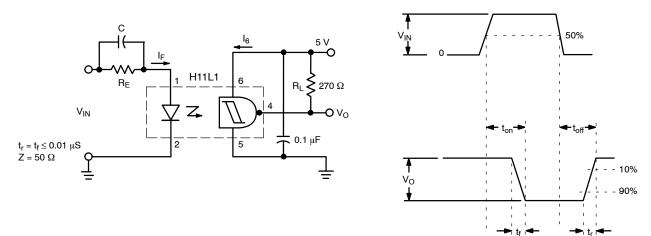
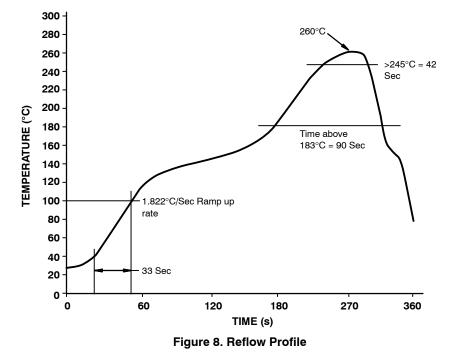


Figure 6. LED Forward Voltage vs. Forward Current

TYPICAL PERFORMANCE CURVES (continued)







REFLOW PROFILE

Part Number	Package	Shipping [†]
H11L1M	DIP 6-Pin	50 Units/Tube
H11L1SM	SMT 6-Pin (Lead Bend)	50 Units/Tube
H11L1SR2M	SMT 6-Pin (Lead Bend)	1000 Units/Tape & Reel
H11L1VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	50 Units/Tube
H11L1SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	50 Units/Tube
H11L1SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	1000 Units/Tape & Reel
H11L1TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option	50 Units/Tube

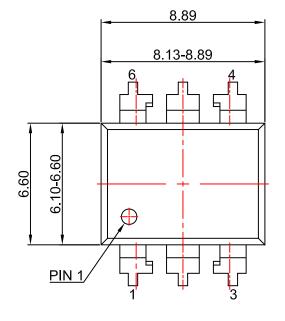
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

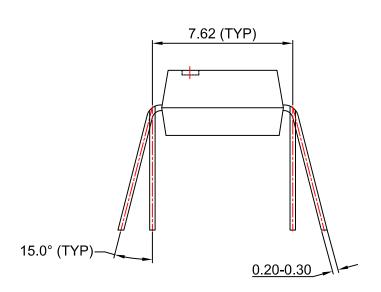
3. The product orderable part number system listed in this table also applies to the H11L2M and H11L3M product families.

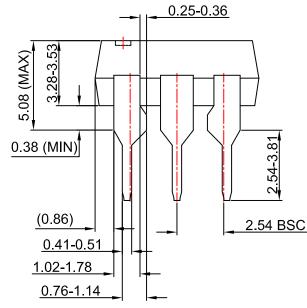


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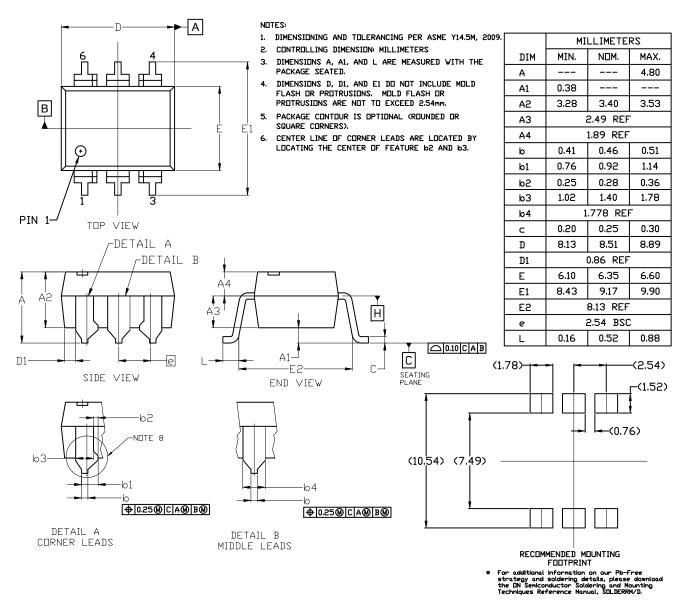
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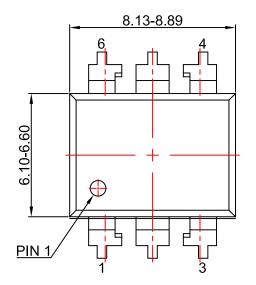


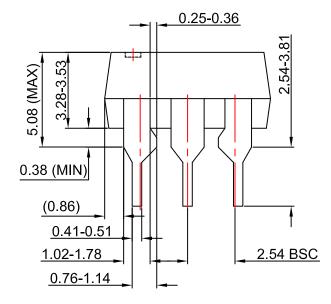
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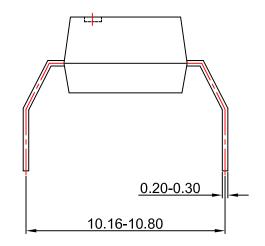


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