

MC4558

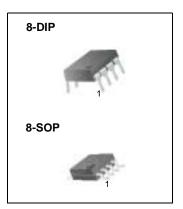
Dual Operational Amplifier

Features

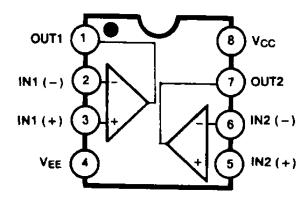
- No frequency compensation required.
- No latch up.
- Large common mode and differential voltage range.
- Parameter tracking over temperature range.
- Gain and phase match between amplifiers.
- Internally frequency compensated.
- Low noise input transistors.

Descriptions

The MC4558 series is a monolithic integrated circuit designed for dual operational amplifier.

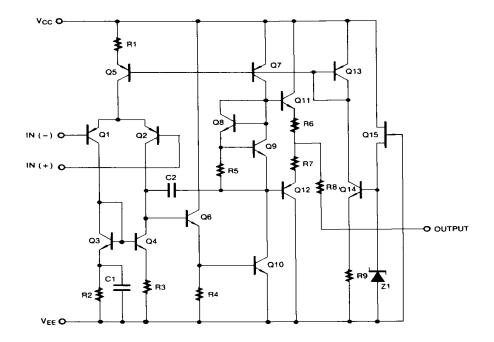


Internal Block Diagram



Schematic Diagram

(One Section Only)



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±22	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage	Vı	±15	V
Power Dissipation	PD	400	mW
Operating Temperature Range MC4558C MC4558V	TOPR	0 ~ 70 -40 ~ 85	°C
Storage Temperature Range	TSTG	-65 ~ 150	°C

Electrical Characteristics

(VCC = 15V, VEE = - 15V, TA = 25 $^{\circ}$ C unless otherwise specified)

Dovernator	C: make al	Symbol Conditions		MC4558C/MC4558V			11	
Parameter	Symbol			Min	Тур	Max	Unit	
Input Offset Voltage V _{IO}		Rs≤10KΩ		-	2	6	mV	
Input Offset Voltage	VIO		Note 1	-	-	7.5	1111	
Input Offset Current				-	5	200		
	lio		TA=TA(MAX)	-	-	300	nA	
			TA = TA(MIN)	-	-	300		
Input Bias Current				-	30	500		
	IBIAS		TA=TA(MAX)	-	-	800	nA	
			TA = TA(MIN)	-	-	800		
Large Signal Gv		$V_{O(P-P)} = \pm 10V, R_{L} \le 2K\Omega$		20	200	-	V/mV	
Voltage Gain	OV		Note 1	-	-	-	7 7/1117	
Common Mode Input	V _{I(R)}			±12	±13	-	V	
Voltage Range	VI(K)		Note 1	-	-	-		
Common Mode	Common Mode CMRR	Rs≤10KΩ		70	90	-	dB	
Rejection Ratio	OWNER		Note 1	-	-	-		
Supply Voltage Rejection Ratio	R _S ≤10KΩ		76	90	-	- dB		
	1 OKK		Note 1	76	90	-	ub	
Output Voltage Swing Vo	VO(P.P)	RL≥10KΩ		±12	±14	-	V	
	VO(P.P)	RL≥2KΩ		±10	±13	-		
Supply Current (Both Amplifiers)				-	3.5	5.8		
	Icc		TA = TA(MAX)	-	-	5.0	mA	
			TA = TA(MIN)	-	-	6.7		
Power Consumption (Both Amplifiers)				-	70	170		
	PC		TA = TA(MAX)	-	-	150	mW	
			$T_a = T_A(MIN)$	-	-	200		
Slew Rate (Note2)	SR	VI =10V, RL≥2KΩ CI≤100pF		1.2	-	-	V/μs	
Rise Time (Note2)	TR	V _I =20mV, R _L ≥2KΩ C _I ≤100pF		-	0.3	-	μs	
Overshoot (Note2)	os	V _I =20mV, R _L ≥2KΩ C _I ≤100pF		-	15	-	%	

Note:

 $^{1. \} MC4558C: T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = 0 \leq T_{A} \leq 70 \ ^{\circ}C \ , \ MC4558V: T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \ ^{\circ}C$

^{2.} Guaranteed by design.

Typical Performance Characteristics

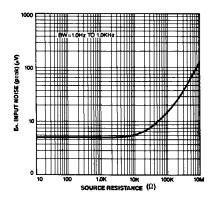


Figure 1. Burst Noise vs Source Resistance

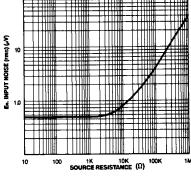


Figure 2. RMS Noise vs Source Resistance

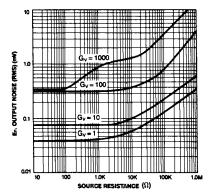


Figure 3. Output Noise vs Source Resistance

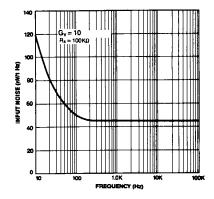


Figure 4. Spectral Noise Density

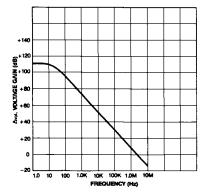


Figure 5. Open Loop Frequency Response

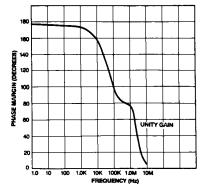


Figure 6. Phase Margin vs Frequency

Typical Performance Characteristics (continued)

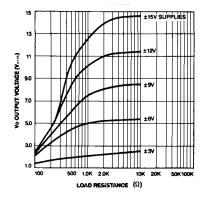


Figure 7. Positive Output Voltage Swing vs Load Resistance

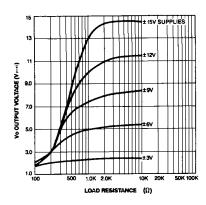


Figure 8. Negative Output Voltage Swing vs Load Resistance

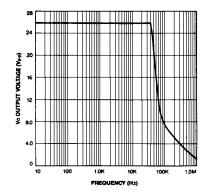
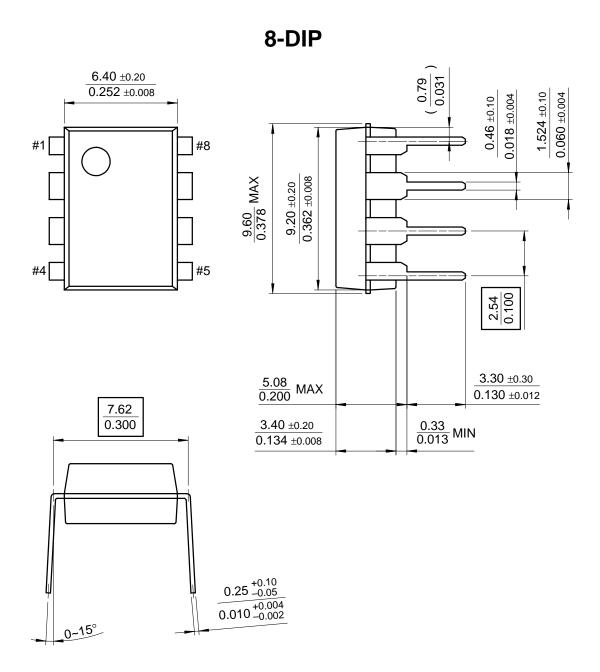


Figure 9. Power Bandwidth (Large Signal Output Swing vs Frequency)

Mechanical Dimensions

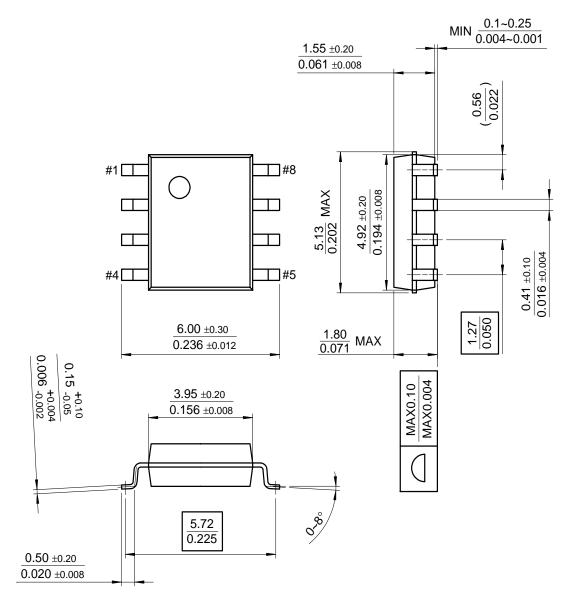
Package



Mechanical Dimensions (Continued)

Package

8-SOP



Ordering Information

Product Number	Package	Operating Temperature	
MC4558CP	8-DIP	0 ~ + 70°C	
MC4558CD	8-SOP	0~+700	
MC4558VP	8-DIP	-40 ~ +85°C	
MC4558VD	8-SOP		

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