

LA6393AM

High-Performance Dual Comparator for Parallel Comparator Circuits

Overview

The LA6393AM is a high-performance dual comparator that features the flexible operating characteristics of a wide supply voltage range (2 to 24 V for single voltage operation) and a wide operating temperature range (-40 to +125 °C). It also features superlative input characteristics and low power, making it optimal for a wide range of applications including automotive and industrial applications.

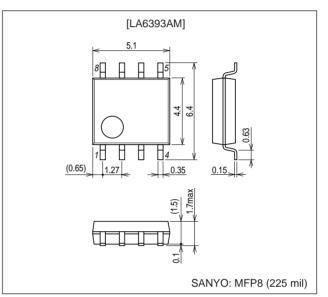
Functions and Features

- Wide operating supply voltage range: 2.0 to 24.0 V (single voltage supply), ±1.0 to 12.0 V (dual voltage supply)
- Wide common-mode input voltage range: 0 to V_{CC} 1.8 V
- Open collector outputs allow the use of wired OR circuits
- Low current drain for low-power operation (0.6 mA)
- Miniature flat package supports product miniaturization

Package Dimensions

unit: mm

3032C-MFP8



Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		36	V
Differential input voltage	VID		36	V
Common-mode input voltage range	VICM		-0.3 to +36	V
Allowable power dissipation	Pd max		300	mW
Operating temperature	Topr		-40 to +125	°C
Storage temperature	Tstg		-55 to +150	°C

Allowable Operating Ranges at Ta = -40° C to $+125^{\circ}$ C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Supply voltage	V _{CC}		2		24	V

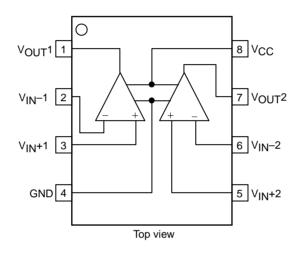
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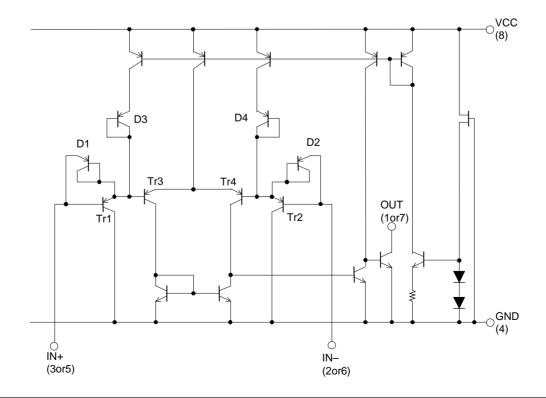
Electrical Characteristics at Ta = $-40^\circ C$ to $+125^\circ C,$ V_{CC} = 5 V

Parameter	Symbol	Conditions	Ratings			11
		Conditions	min	typ	max	Unit
Input offset voltage	V _{IO}			±1	±5	mV
Input offset current	I _{IO}			±5	±50	nA
Input bias current	Ι _Β			25	250	nA
Common-mode input voltage range	VICM		0		V _{CC} – 1.8	V
Current drain	Icc	$R_L = \infty$		0.6	1	mA
Voltage gain	VG	$R_L = 15 \text{ k}\Omega$		200		V/mV
Response time	SR	$VRL = 5 V, R_L = 5.1 k\Omega$		1.3		μs
Output sink current	I _{SINK}	V_{IN}^{-} = 0.5 V, V_{IN}^{+} = 0 V, $V_O \le 1.5$ V	6	16		mA
Output saturation voltage	V _{OL}	V_{IN}^{-} = 0.5 V, V_{IN}^{+} = 0 V, ISINK \leq 3 mA		0.2	0.4	V
Output leakage current	I _{LEAK}	$V_{IN}^{-} = 0 \text{ V}, V_{IN}^{-} = 0.5 \text{ V}, V_{O} = 5 \text{ V}$		0.1		nA

Pin Assignment



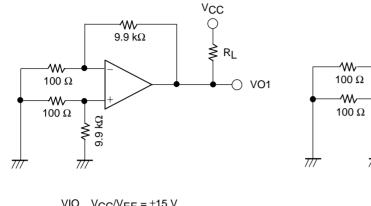
Equivalent Circuit Block Diagram



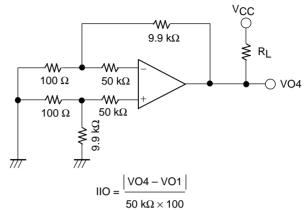
Test Circuits

1. Input offset voltage

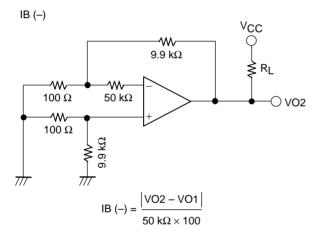
2. Input offset current

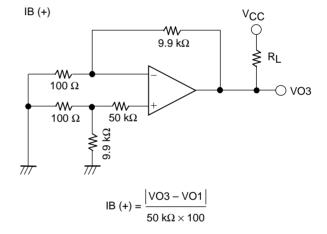


 $\begin{array}{ll} \text{VIO} & \text{V}_{\text{CC}}/\text{V}_{\text{EE}} = \pm 15 \text{ V} \\ \text{VIO} = \text{VO1}/100 \end{array}$

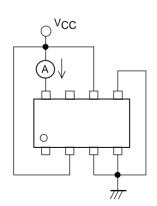


3. Input bias current

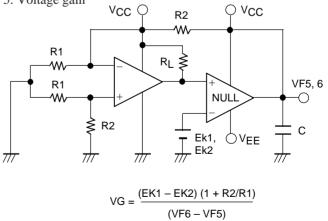




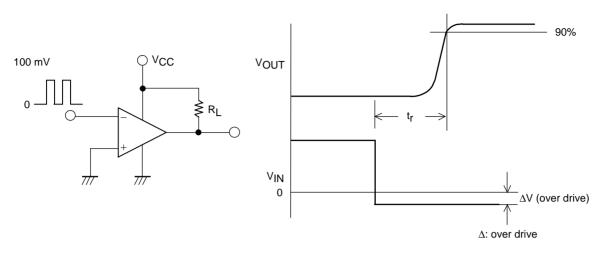
4. Current drain



5. Voltage gain

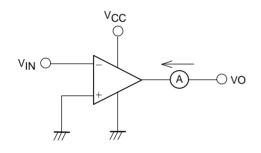


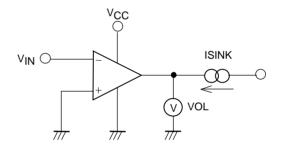
6. Response time



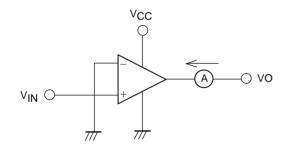
7. Output sink current

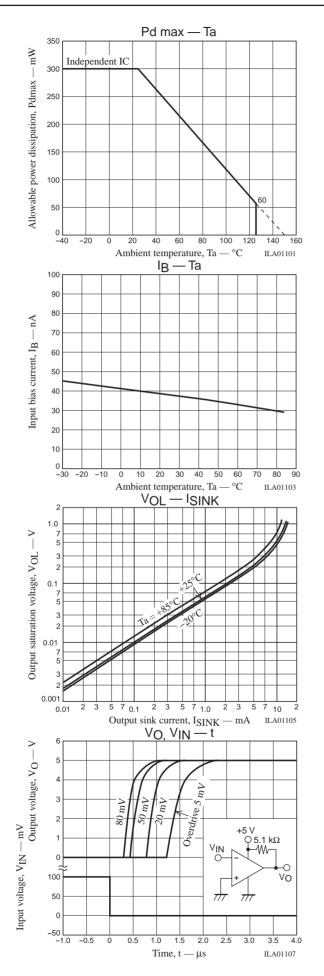
8. Output saturation voltage

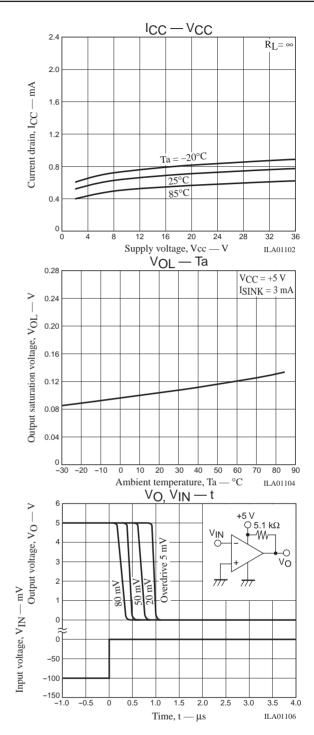




9. Output leakage current







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