TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSV)

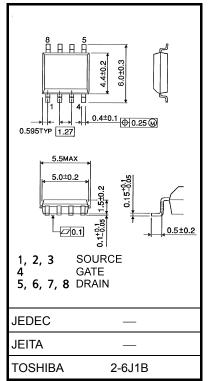
TPC8119

Lithium-Ion Battery Applications Load switch Applications Notebook PC Applications

- Small footprint due to a small and thin package
- Low drain-source ON-resistance: R_{DS} (ON) = 10 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 24 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -30 \ V)$
- Enhancement mode: V_{th} = -0.8 to -2.0 V (V_{DS} = -10 V, I_D = -1 mA)

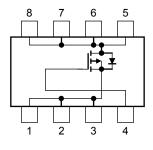
Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-30	V
Drain-gate voltage (R	t _{GS} = 20 kΩ)	V _{DGR}	-30	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ID	-10	А
Drain current	Pulse (Note 1)	I _{DP}	-40	A
Drain power dissipati	on (t = 10 s) (Note 2a)	PD	1.9	W
Drain power dissipati	on (t = 10 s) (Note 2b)	PD	1.0	W
Single pulse avalanche energy (Note 3)		E _{AS}	67	mJ
Avalanche current		I _{AR}	-10	А
Repetitive avalanche (energy Note 2a) (Note 4)	E _{AR}	0.030	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	–55 to 150	°C

Absolute Maximum Ratings (Ta = 25°C)



Weight: 0.080 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Please handle with care.

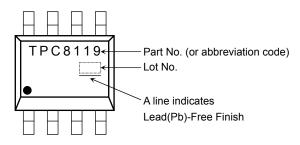
Unit: mm

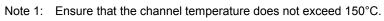
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Thermal Characteristics

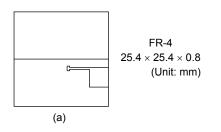
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W	
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W	

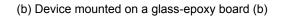
Marking (Note 5)

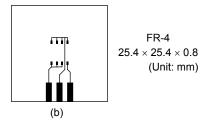




Note 2: (a) Device mounted on a glass-epoxy board (a)







- Note 3: $V_{DD} = -24$ V, $T_{ch} = 25^{\circ}C$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = -10A$
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5: on lower left of the marking indicates Pin 1.
 - ※ Weekly code: (Three digits)



Week of manufacture (01 for the first week of the year: sequential number up to 52 or 53)

 Year of manufacture (The last digit of the year)

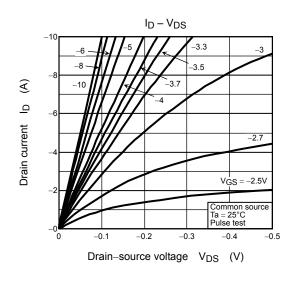
Electrical Characteristics (Ta = 25°C)

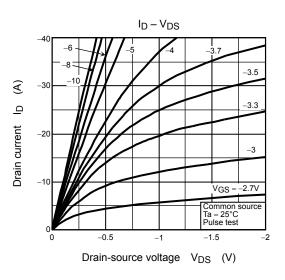
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rrent	I _{GSS}	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	_		±100	nA
Drain cut-OFF cu	irrent	I _{DSS}	$V_{DS} = -30$ V, $V_{GS} = 0$ V			-10	μA
Drain-source breakdown voltage Gate threshold voltage Drain-source ON resistance Forward transfer admittance Input capacitance Reverse transfer capacitance Output capacitance Rise time Rise time	V (BR) DSS	$I_{D} = -10$ mA, $V_{GS} = 0$ V	-30			V	
Drain-source bre	akdown vollage	V (BR) DSX	$I_D = -10$ mA, $V_{GS} = 20$ V	±100 1 1	v		
Gate threshold ve	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	20 28 10 13 12 24		-2.0	V
Drain course ON	registance	R _{DS (ON)}	$V_{GS} = -4 \text{ V}, \text{ I}_{D} = -5 \text{ A}$		20	28	mΩ
Drain-source ON	ate threshold voltage ain-source ON resistance rward transfer admittance out capacitance everse transfer capacitance utput capacitance Rise time Turn-ON time		$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -5 \text{ A}$		10	13	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	12	24		S
Input capacitance		C _{iss}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		1560		pF
Reverse transfer capacitance		C _{rss}			370		
Output capacitance		C _{oss}			475		
	Rise time	tr		_	8	_	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		16					
	Fall time	t _f	R = 3.0	_	55	_	- ns
	Turn-OFF time	t _{off}	55	_	145		
		Qg		_	40	_	nC
Gate-source charge 1		Q _{gs1}			5		
		Q _{gd}]	_	13	—	

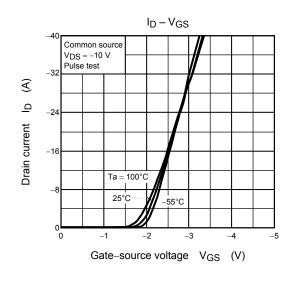
Source-Drain Ratings and Characteristics (Ta = 25°C)

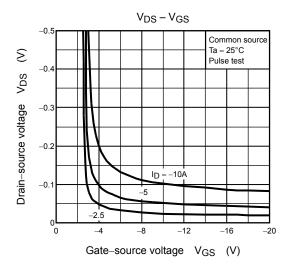
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	_	_	-40	А
Forward voltage (diode)			V _{DSF}	$I_{DR} = -10 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$			1.2	V

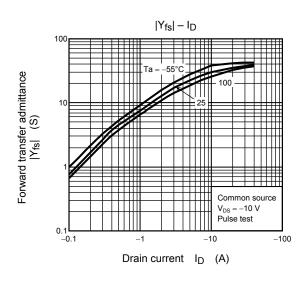
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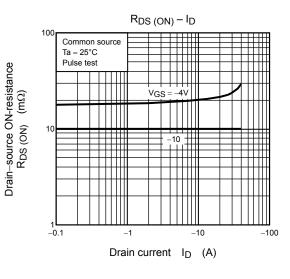




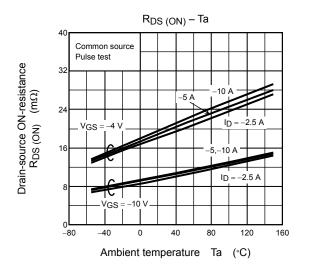


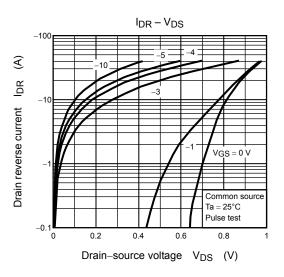


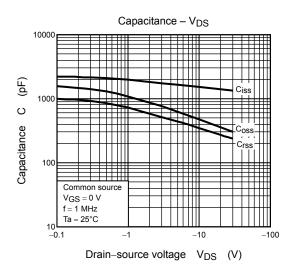


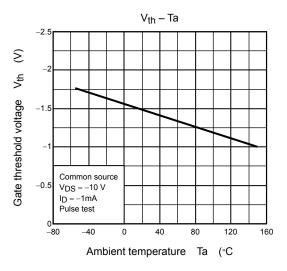


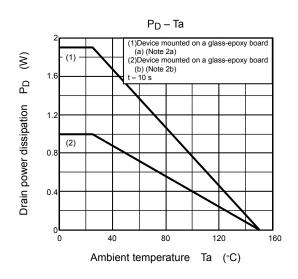
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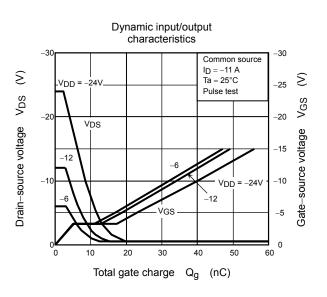


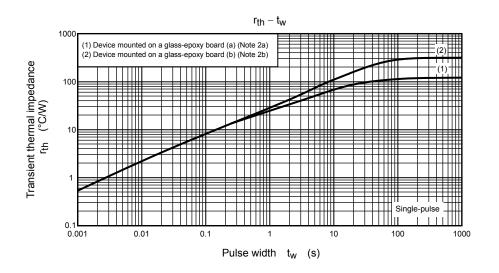


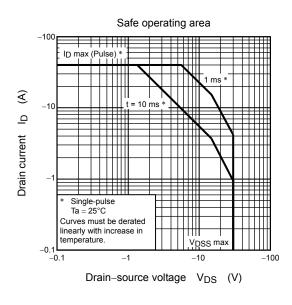












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